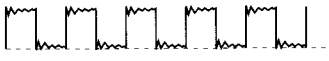
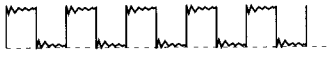



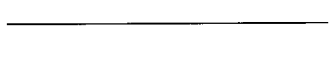
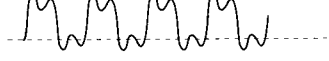
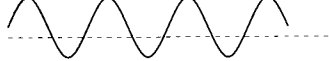
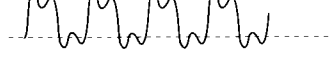
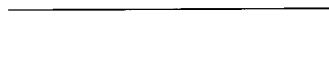




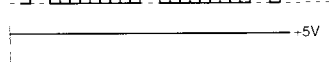
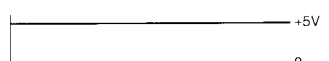
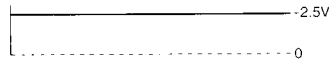



Table 7.2 Signals measured on microprocessor D1201

Logic 0=0V, Logic 1=+5V

Comp	Name (pin) Circ /IC	Scope	Freq. Per.	Data H/L/A	Description
D1201					
1	AD01 /AD1/P3		1.5 MHz	A	Address Data1/Address Data1 port3
2	AD00 /AD0/P3		2 MHz	A	Address Data0/Address Data0 port3
3	RDIN /RD		2 MHz	A	Read not/Read
4	ADV N /ALE/ADV		2 MHz	A	Address valid not/Address latch enable/Address valid output
5	/INST		200 kHz	A	/Instruction fetch for external memory
6	/BUSWIDTH		0	L	/Buswidth selection, 8 or 16 bit
7	μPCLKOUT/CLKOUT		6.25 MHz	A	μP clock out/clock out 1/2 oscillation frequency 50% duty cycle
8	/X-TAL2		12.5 MHz	A	/Crystal
9	/X-TAL1		12.5 MHz	A	/Crystal
10	/VSS		0	L	/Voltage supply ground (0)
11	/VSS		0	L	/Voltage supply ground (0)
12	/VCC		0	H	/Main supply (+5V)
13	/VCC		0	H	/Main supply (+5V)
14	/EA		0	H	/External access
15	/NMI			A	/NonMaskable Interrupt
16	/PO.3		0	H	/port 0.3
17	/ACH1		0	H	/Analog channel 1
18	/ACH0		0	H	/Analog channel 0